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BayFOR Informationsveranstaltung
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Chair of Systems and Networking

- ▶ Principal Research Interests
 - Hard Real-time in Processor Architecture and System Software, Multi- and Many-cores
 - Safety and Reliability for Manycores and Embedded Systems

- ▶ Main Expertise
 - SystemC simulation and FPGA prototyping of SMT processors and multi-cores
 - Hard real-time system software for application studies
 - Safety, reliability, and debugging support in hw and sw

Current EC Research Projects at Chair of Systems and Networking

- ▶ **MERASA - Multi-Core Execution of Hard Real-Time Applications Supporting Analysability**
 - Project coordination by Theo Ungerer, 5 partners
 - EC-FP7 STREP in ICT, 1.11.2007 – 31.10.2010
 - 2,1 Mio EC contribution

- ▶ **TERAFLUX**
 - EC-FP7 STREP in „Future and Emerging Technology“, 1.1.2010 – 31.12.2013
 - 10 partners, UAU part: Reliability and fault-tolerance in Teradevice many-cores

- ▶ **ACROSS**
 - ARTEMIS project, 1.4.2010 – 31.3.2012
 - 23 partners, only 3 of universities
 - UAU part: Interfacing of multithreaded CarCore to real-time interconnects

Some Experiences

- ▶ **Searching for partners**
 - Start early, look for a clear topic
 - At least 3 partners from 3 different countries required
 - Industry, in particular SMEs (small and medium enterprises) required
- ▶ **Project proposal**
 - Follow exactly the required proposal form
 - Do not focus totally on the technical part, dissemination and management are also very important
 - Meet exactly the requirements of the Call – strengthen competitiveness of European industry
 - Define „measurable objectives“
 - Clear work package structure
 - Not too many Deliverables
 - Deliverable dates in coherence with yearly review meetings and milestones